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Inventor Information for 10/022659

Inventor Name	City	State/Country
AZADET, KAMERAN	MORGANVILLE	NEW JERSEY
HARATSCH, ERICH FRANZ	BRADLEY BEACH	NEW JERSEY

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Inventor Name Search Result

Your Search was:

Last Name = AZADET

First Name = KAMERAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08426510	5625304	150	04/21/1995	VOLTAGE COMPARATOR REQUIRING NO COMPENSATING OFFSET VOLTAGE	AZADET, KAMERAN
08713687	5983254	150	09/17/1996	ZERO-LATENCY PIPELINE ARCHITECTURE FOR DIGITAL FILTERS	AZADET, KAMERAN
09188625	6584159	150	11/09/1998	MIXED-MODE NEXT/ECHO CANCELLER FOR PULSE AMPLITUDE MODULATED (PAM) SIGNALS	AZADET, KAMERAN
09203969	Not Issued	161	12/02/1998	METHOD AND APPARATUS FOR REDUCING DISPERSION PROBLEMS IN FIBER CHANNELS	AZADET, KAMERAN
09206519	6202075	150	12/07/1998	SECOND ORDER LMS TAP UPDATE ALGORITHM WITH HIGH TRACKING CAPABILITY	AZADET, KAMERAN
09206527	6363112	150	12/07/1998	PARALLEL PROCESSING DECISION FEEDBACK EQUALIZER	AZADET, KAMERAN
09326781	6192072	150	06/04/1999	PARALLEL PROCESSING DECISION-FEEDBACK EQUALIZER (DFE) WITH LOOK-AHEAD PROCESSING	AZADET, KAMERAN
09414129	6617993	150	10/08/1999	ANALOG TO DIGITAL CONVERTER USING ASYNCHRONOUSLY SWEPT THERMOMETER CODES	AZADET, KAMERAN
09471920	6999521	150	12/23/1999	METHOD AND APPARATUS FOR SHORTENING THE CRITICAL PATH OF REDUCED COMPLEXITY SEQUENCE	AZADET, KAMERAN

				ESTIMATION TECHNIQUES	
<u>09834668</u>	<u>7000175</u>	150	04/13/2001	METHOD AND APPARATUS FOR PIPELINED JOINT EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	AZADET, KAMERAN
<u>10022659</u>	Not Issued	120	12/18/2001	Method and apparatus for joint equalization and decoding of multilevel codes	AZADET, KAMERAN
<u>10022665</u>	Not Issued	133	12/18/2001	Method and apparatus for joint equalization and decoding of multidimensional codes transmitted over multiple symbol durations	AZADET, KAMERAN
<u>10106990</u>	<u>6771198</u>	150	03/25/2002	METHOD AND APPARATUS FOR CONVERTING BETWEEN ANALOG AND DIGITAL DOMAINS USING FREQUENCY INTERLEAVING	AZADET, KAMERAN
<u>10122997</u>	<u>7085794</u>	150	04/12/2002	LOW POWER VECTOR SUMMATION METHOD AND APPARATUS	AZADET, KAMERAN
<u>10219904</u>	<u>7155134</u>	150	08/15/2002	PULSE AMPLITUDE MODULATED TRANSMISSION SCHEME FOR OPTICAL CHANNELS WITH SOFT DECISION DECODING	AZADET, KAMERAN
<u>10219905</u>	Not Issued	71	08/15/2002	Optically calibrated pulse amplitude modulated transmission scheme for optical channels	AZADET, KAMERAN
<u>10219906</u>	Not Issued	71	08/15/2002	Method and apparatus for cross-talk cancellation in frequency division multiplexed transmission systems	AZADET, KAMERAN
<u>10229623</u>	Not Issued	41	08/28/2002	Single sideband dense wavelength division multiplexed optical transmission scheme	AZADET, KAMERAN
<u>10348871</u>	<u>7164711</u>	150	01/22/2003	PROGRAMMABLE RECEIVE-SIDE CHANNEL EQUALIZER	AZADET, KAMERAN
<u>10378096</u>	<u>7257329</u>	150	02/28/2003	DUOBINARY PULSE SHAPING FOR OPTICAL TRANSMISSION SYSTEMS EMPLOYING PULSE AMPLITUDE MODULATION TECHNIQUES	AZADET, KAMERAN
<u>10610334</u>	Not	41	06/30/2003	Method and apparatus for reducing	AZADET,

	Issued			cross-talk with reduced redundancies	KAMERAN
<u>10610335</u>	Not Issued	41	06/30/2003	Method and apparatus for reducing noise in an unbalanced channel using common mode component	AZADET, KAMERAN
<u>10610336</u>	7263541	150	06/30/2003	MULTI-DIMENSIONAL HYBRID AND TRANSPOSE FORM FINITE IMPULSE RESPONSE FILTERS	AZADET, KAMERAN
<u>10874834</u>	Not Issued	41	06/23/2004	Method and apparatus for power management using transmission mode with reduced power	AZADET, KAMERAN
<u>10880331</u>	Not Issued	71	06/29/2004	Universal controller for peripheral devices in a computing system	AZADET, KAMERAN
<u>10924280</u>	Not Issued	123	08/23/2004	Peripheral controller with shared EEPROM	AZADET, KAMERAN
<u>10945663</u>	Not Issued	30	09/21/2004	Methods and apparatus for interface adapter integrated virus protection	AZADET, KAMERAN
<u>11141337</u>	Not Issued	41	05/31/2005	Common-mode shifting circuit for CML buffers	AZADET, KAMERAN
<u>11191895</u>	Not Issued	30	07/28/2005	Digital phase-locked loop	AZADET, KAMERAN
<u>11234446</u>	Not Issued	61	09/26/2005	Method and apparatus for pipelined joint equalization and decoding for gigabit communications	AZADET, KAMERAN
<u>11256182</u>	Not Issued	30	10/21/2005	Hybrid memory architecture for reduced state sequence estimation (RSSE) techniques	AZADET, KAMERAN
<u>11359201</u>	Not Issued	71	02/22/2006	Low power vector summation method and apparatus	AZADET, KAMERAN
<u>11561296</u>	Not Issued	30	11/17/2006	METHODS AND APPARATUS FOR SOFT DECISION DECODING USING RELIABILITY VALUES BASED ON A LOG BASE TWO FUNCTION	AZADET, KAMERAN
<u>11781313</u>	Not Issued	17	07/23/2007	Multi-Dimensional Hybrid And Transpose Form Finite Impulse Response Filters	AZADET, KAMERAN
<u>60180079</u>	Not Issued	159	02/03/2000	Zero-overhead coding for LAN serial and WDM solutions	AZADET, KAMERAN
<u>60245519</u>	Not Issued	159	11/03/2000	Pipelined joint equalization and decoding for gigabit	AZADET, KAMERAN

				communications	
60347462	Not Issued	159	01/11/2002	Frequency interleaved analog to digital conversion	AZADET, KAMERAN
60366923	Not Issued	159	03/22/2002	Bandwidth efficient multicarrier transmission system with cross-talk cancellation	AZADET, KAMERAN
60366930	Not Issued	159	03/22/2002	Opto-electronically calibrated PAM transceiver	AZADET, KAMERAN
60387828	Not Issued	159	06/10/2002	Dense wavelength division multiplexing (DWDM) single sideband (SSB) optical transmission scheme based on optical MUX/DEMUX	AZADET, KAMERAN
60412870	Not Issued	159	09/23/2002	Duobinary pulse shaping for PAM transmission over optical channels	AZADET, KAMERAN
60443258	Not Issued	159	01/28/2003	Multi-dimensional hybrid-form FIR filter architecture	AZADET, KAMERAN
60443260	Not Issued	159	01/28/2003	Zero-redundancy cross-talk cancellation architecture	AZADET, KAMERAN
60443261	Not Issued	159	01/28/2003	Cross-talk canceller and equalizer architecture for unbalanced channels	AZADET, KAMERAN
60525229	Not Issued	159	11/25/2003	Universal controller for PC peripherals	AZADET, KAMERAN
60525230	Not Issued	159	11/25/2003	Low pin-count controller with shared EEPROM	AZADET, KAMERAN
60525231	Not Issued	159	11/25/2003	Software-directed power management for Ethernet network device	AZADET, KAMERAN

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Inventor Name Search Result

Your Search was:

Last Name = HARATSCH

First Name = ERICH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09031728</u>	6154222	150	02/27/1998	METHOD FOR DEFINING ANIMATION PARAMETERS FOR AN ANIMATION DEFINITION INTERFACE	HARATSCH, ERICH
<u>09113747</u>	Not Issued	71	07/10/1998	INTERACTION MODALITIES FOR MULTIMEDIA DELIVERY AND PRESENTATION	HARATSCH, ERICH
<u>09113748</u>	6317131	150	07/10/1998	INTERACTION MODALITIES FOR MULTIMEDIA DELIVERY AND PRESENTATION USING NODES	HARATSCH, ERICH
<u>09686077</u>	6483513	150	10/12/2000	Method for defining MPEP 4 animation parameters for an animation definition interface	HARATSCH, ERICH
<u>09950792</u>	Not Issued	161	09/13/2001	Interaction modalities for multimedia delivery and presentation using nodes	HARATSCH, ERICH
<u>09975052</u>	Not Issued	161	10/12/2001	Method for defining animation parameters for an animation definition interface	HARATSCH, ERICH
<u>10291428</u>	6970172	150	11/12/2002	SYSTEM FOR DEFINING MPEG 4 ANIMATION PARAMETERS FOR AN ANIMATION DEFINITION INTERFACE	HARATSCH, ERICH
<u>11179715</u>	Not Issued	93	07/12/2005	METHOD FOR DEFINING ANIMATION PARAMETERS FOR AN ANIMATION DEFINITION INTERFACE	HARATSCH, ERICH
<u>60041732</u>	Not Issued	159	03/27/1997	ANIMATION DEFINITION INTERFACE	HARATSCH, ERICH

<u>60042799</u>	Not Issued	159	04/07/1997	LET ANIMALS AND FURNITURE SPEAK: PROPOSAL FOR EXTENDING THE SCOPE OF FACE AND BODY ANIMATION	HARATSCH, ERICH
<u>60052576</u>	Not Issued	159	07/15/1997	ENSURING QUALITY OF SERVICE FOR SCENE GRAPHS WITH BIFS	HARATSCH, ERICH
<u>11496121</u>	Not Issued	25	07/31/2006	Methods and apparatus for low-density parity check decoding using hardware-sharing and serial sum-product architecture	HARATSCH, ERICH F.
<u>11535540</u>	Not Issued	25	09/27/2006	Systems and Methods for Generating Erasure Flags	HARATSCH, ERICH F.
<u>11620988</u>	Not Issued	25	01/08/2007	SYSTEMS AND METHODS FOR PRIORITIZING ERROR CORRECTION DATA	HARATSCH, ERICH F.
<u>09834668</u>	<u>7000175</u>	150	04/13/2001	METHOD AND APPARATUS FOR PIPELINED JOINT EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	HARATSCH, ERICH FRANZ
<u>10022659</u>	Not Issued	120	12/18/2001	Method and apparatus for joint equalization and decoding of multilevel codes	HARATSCH, ERICH FRANZ
<u>10022665</u>	Not Issued	133	12/18/2001	Method and apparatus for joint equalization and decoding of multidimensional codes transmitted over multiple symbol durations	HARATSCH, ERICH FRANZ
<u>10853087</u>	Not Issued	71	05/25/2004	Method and apparatus for multiple step viterbi detection with local feedback	HARATSCH, ERICH FRANZ
<u>10853088</u>	Not Issued	41	05/25/2004	Method and apparatus for precomputation and pipelined selection of intersymbol interference estimates in a reduced-state Viterbi detector	HARATSCH, ERICH FRANZ
<u>10853089</u>	Not Issued	71	05/25/2004	Method and apparatus for precomputation and pipelined selection of branch metrics in a reduced-state Viterbi detector	HARATSCH, ERICH FRANZ
<u>10853090</u>	Not Issued	161	05/25/2004	Method and apparatus for reduced-state Viterbi detection in a read channel of a magnetic recording system	HARATSCH, ERICH FRANZ

<u>10962188</u>	Not Issued	71	10/08/2004	Pipelined decision-feedback unit in a reduced-state viterbi detector with local feedback	HARATSCH, ERICH FRANZ
<u>11045585</u>	Not Issued	30	01/28/2005	Method and apparatus for soft-output viterbi detection using a multiple-step trellis	HARATSCH, ERICH FRANZ
<u>11119038</u>	Not Issued	30	04/29/2005	Method and apparatus for iterative error-erasure decoding	HARATSCH, ERICH FRANZ
<u>11234446</u>	Not Issued	61	09/26/2005	Method and apparatus for pipelined joint equalization and decoding for gigabit communications	HARATSCH, ERICH FRANZ
<u>11256182</u>	Not Issued	30	10/21/2005	Hybrid memory architecture for reduced state sequence estimation (RSSE) techniques	HARATSCH, ERICH FRANZ
<u>11691847</u>	Not Issued	25	03/27/2007	METHOD AND APPARATUS FOR REDUCED-STATE VITERBI DETECTION IN A READ CHANNEL OF A MAGNETIC RECORDING SYSTEM	HARATSCH, ERICH FRANZ
<u>60244453</u>	Not Issued	159	10/30/2000	Low complexity joint equalizer and decoder for 1000base-t gigabit ethernet	HARATSCH, ERICH FRANZ
<u>60245519</u>	Not Issued	159	11/03/2000	Pipelined joint equalization and decoding for gigabit communications	HARATSCH, ERICH FRANZ
<u>09326785</u>	<u>6690754</u>	150	06/04/1999	METHOD AND APPARATUS FOR REDUCING THE COMPUTATIONAL COMPLEXITY AND RELAXING THE CRITICAL PATH OF REDUCED STATE SEQUENCE ESTIMATION (RSSE) TECHNIQUES	HARATSCH, ERICH FRANZ
<u>09471920</u>	<u>6999521</u>	150	12/23/1999	METHOD AND APPARATUS FOR SHORTENING THE CRITICAL PATH OF REDUCED COMPLEXITY SEQUENCE ESTIMATION TECHNIQUES	HARATSCH, ERICH FRANZ

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	HARATSCH	ERICH	

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Customer Number	Contact Information	Address
No Customer # Delivery Mode: <u>PAPER</u>	Telephone: No Telephone # Fax: No Fax # E-Mail: No E-Mail Address	Ryan, Mason & Lewis, LLP Suite 205 1300 Post Road Fairfield CT 06430

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